



Shielded Power Inductors – XGL3515



- Industry's lowest DCR and ultra low AC losses
- AEC-Q200 qualified
- Superior current handling with soft saturation characteristics
- Wide inductance range from 0.090 to 2.2 μ H

Designer's Kit C503 contains 3 of each value.

Core material Composite

Core and winding loss See www.coilcraft.com/coreloss

Environmental RoHS compliant, halogen free

Terminations RoHS compliant tin-silver (96.5/3.5) over copper. Other terminations available at additional cost.

Weight: 0.082 – 0.090 g

Operating voltage: 80 V⁷

Ambient temperature –40°C to +125°C with (40°C rise) Irms current.

Maximum part temperature +165°C (ambient + temp rise). [Derating](#).

Storage temperature Component: –55°C to +165°C.

Tape and reel packaging: –55°C to +80°C

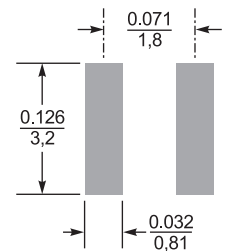
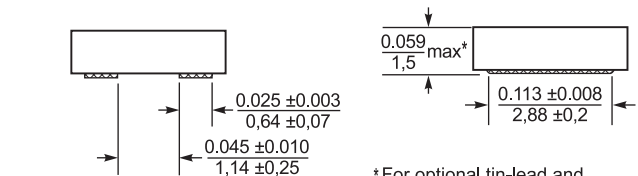
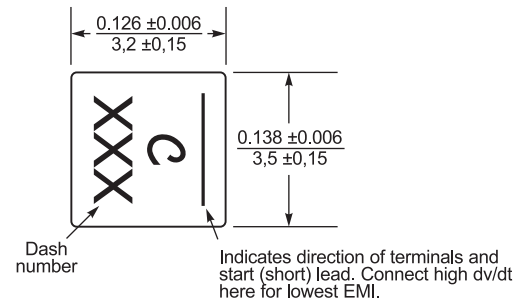
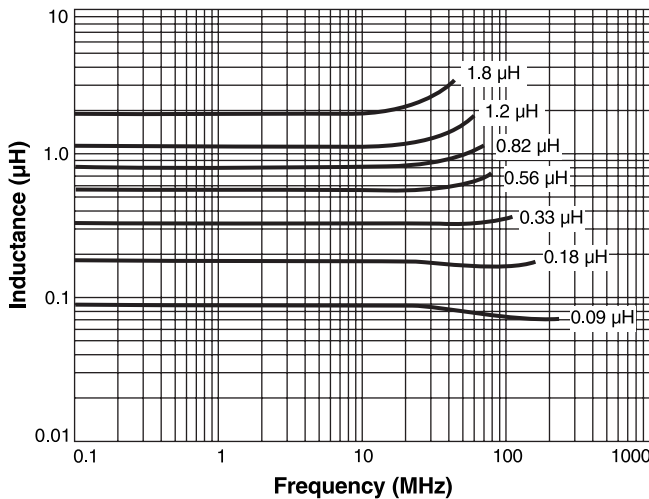
Resistance to soldering heat Max three 40 second reflows at +260°C, parts cooled to room temperature between cycles

Moisture Sensitivity Level (MSL) 1 (unlimited floor life at <30°C / 85% relative humidity)

Packaging 1000/7" reel; 3500/13" reel Plastic tape: 12 mm wide, 0.23 mm thick, 8 mm pocket spacing, 1.8 mm pocket depth

PCB washing Tested to MIL-STD-202 Method 215 plus an additional aqueous wash. See [Doc787_PCB_Washing.pdf](#).

Typical L vs Frequency



Recommended Land Pattern

Dimensions are in $\frac{\text{inches}}{\text{mm}}$



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Part number ¹	Inductance ² ±20% (µH)	DCR (mOhms) ³		SRF typ ⁴ (MHz)	Isat (A) ⁵			Irms (A) ⁶	
		nom	max		10% drop	20% drop	30% drop	20°C rise	40°C rise
XGL3515-900ME_	0.090	2.2	2.6	350	7.6	12.6	17.8	19.5	26.6
XGL3515-101ME_	0.10	2.4	2.8	320	7.5	12.0	16.8	17.0	23.4
XGL3515-181ME_	0.18	3.5	4.1	205	5.4	8.8	12.4	15.1	20.4
XGL3515-221ME_	0.22	4.8	5.6	200	4.7	8.0	11.1	11.8	16.1
XGL3515-331ME_	0.33	6.4	7.4	145	3.9	6.3	8.7	11.0	14.8
XGL3515-451ME_	0.45	8.2	9.5	120	3.2	5.2	7.4	9.6	12.9
XGL3515-561ME_	0.56	10.2	11.8	100	3.0	4.8	6.6	8.1	10.9
XGL3515-681ME_	0.68	11.5	13.3	90	2.8	4.5	6.4	6.9	9.5
XGL3515-821ME_	0.82	16.0	18.4	82	2.4	3.9	5.3	6.3	8.5
XGL3515-102ME_	1.0	18.5	21.3	72	2.2	3.5	4.9	5.9	7.9
XGL3515-122ME_	1.2	23.6	27.2	70	2.0	3.2	4.3	5.1	6.8
XGL3515-152ME_	1.5	31.5	36.3	60	1.8	2.8	3.9	4.6	6.1
XGL3515-182ME_	1.8	36.1	41.6	50	1.5	2.4	3.3	4.0	5.4
XGL3515-222ME_	2.2	40.6	46.7	45	1.4	2.3	3.2	3.8	5.1

1. When ordering, please specify **termination** and **packaging** codes:

XGL3515-222MEC

Termination: **E** = RoHS compliant tin-silver over copper.

Special order: **T** = RoHS tin-silver-copper (95.5/4/0.5) or **S** = non-RoHS tin-lead (63/37).

Packaging: **C** = 7" machine-ready reel. EIA-481 embossed plastic tape (1000 per full reel).
Quantities less than full reel available: in tape (not machine ready) or with leader and trailer (\$25 charge).

D = 13" machine-ready reel. EIA-481 embossed plastic tape (3500 per full reel).
Factory order only, not stocked.

2. Inductance tested at 1 MHz, 0.1 Vrms, 0 Adc.

3. DCR measured on a micro-ohmmeter.

4. SRF measured using Agilent/HP 4395A or equivalent.

5. DC current at 25°C that causes the specified inductance drop from its value without current.

[Click for temperature derating information.](#)

6. Current that causes the specified temperature rise from 25°C ambient. This information is for reference only and does not represent absolute maximum ratings.

[Click for temperature derating information.](#)

7. Voltage capability varies by part number and in many cases may be higher than the listed voltage.

8. Electrical specifications at 25°C.

Refer to Doc 362 "Soldering Surface Mount Components" before soldering.

Irms Testing

Irms testing was performed on 0.75 inch wide × 0.25 inch thick copper traces in still air.

Temperature rise is highly dependent on many factors including pcb land pattern, trace size, and proximity to other components. Therefore temperature rise should be verified in application conditions.



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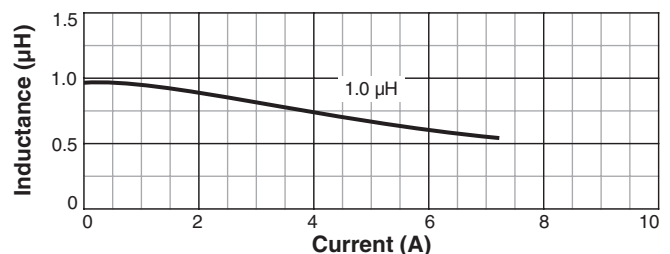
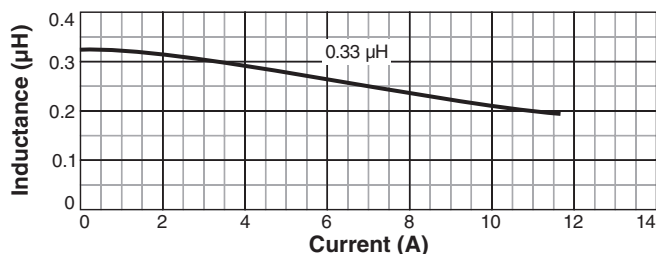
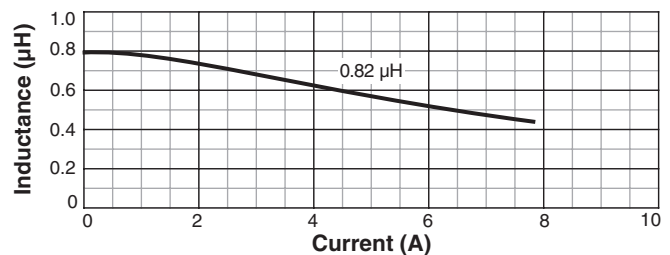
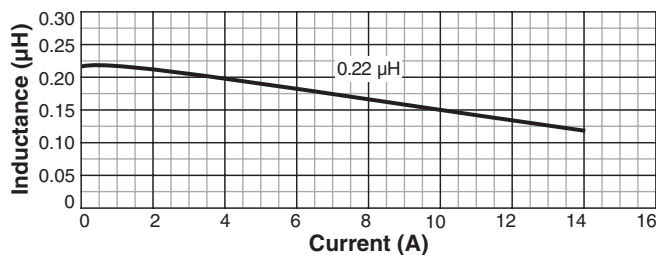
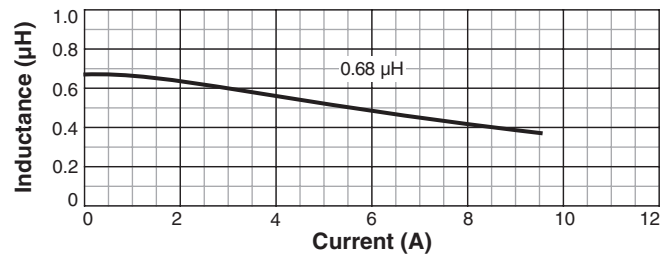
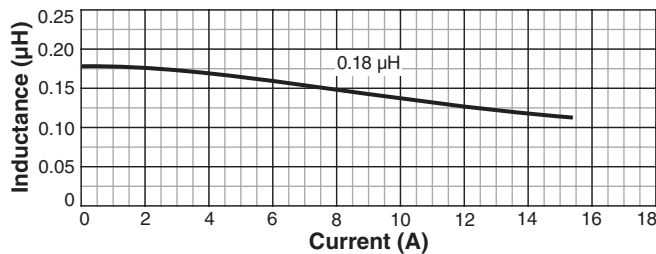
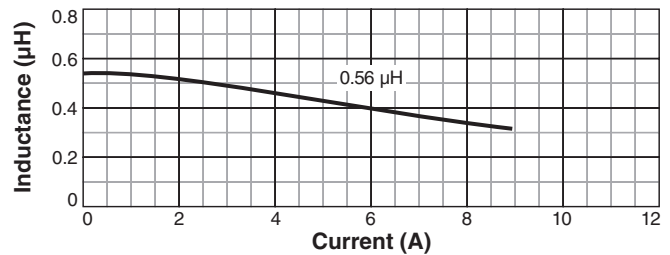
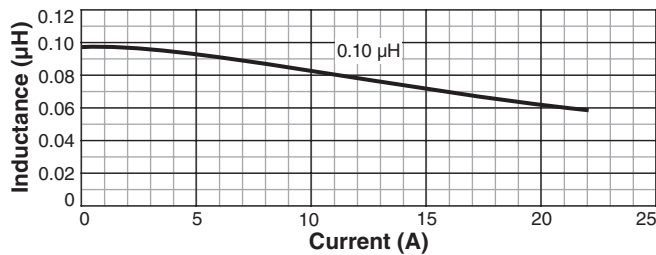
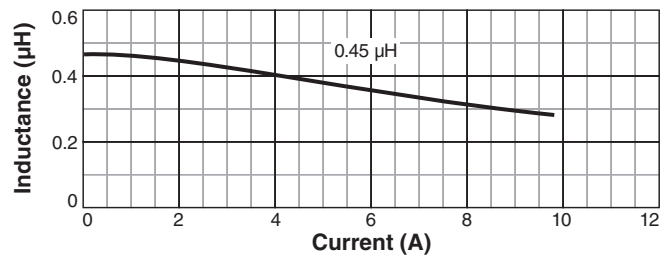
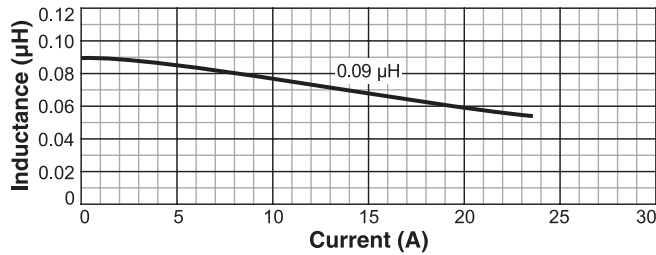
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L vs Current



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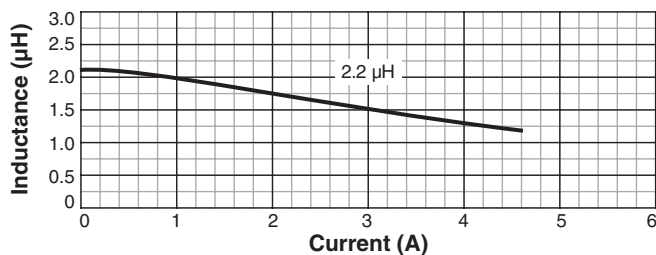
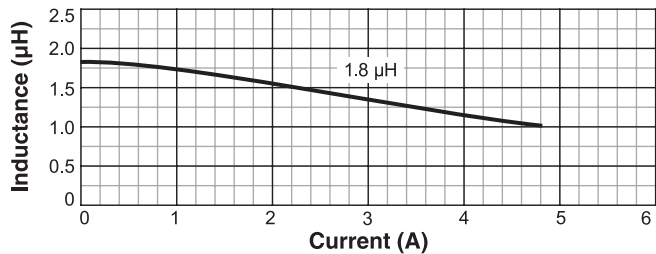
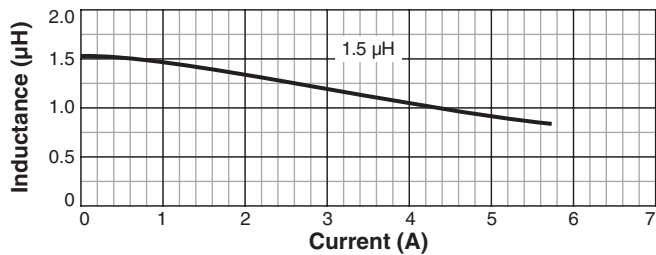
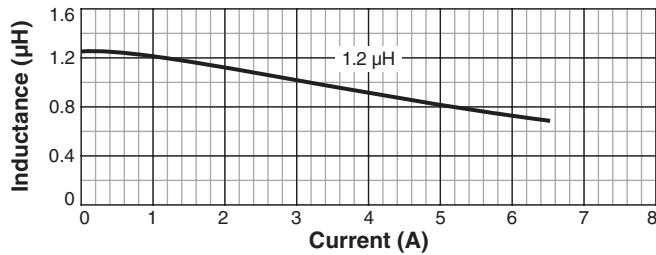
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